

REMARKS

Claims 1-8 are pending in the application. Claims 1-8 are rejected. Claims 1, 4 and 7 have been amended.

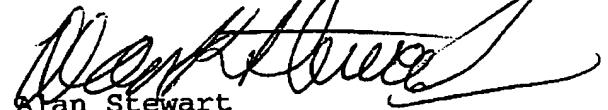
The drawings were objected under 37 CFR 1.83(a). The transistor for the voltage reference generator in claim 7 is transistor 104 in Figure 1. Therefore, the drawings are believed to meet the requirements of 37 CFR 1.83(a).

Claims 1-8 were rejected under 35 USC 112, second paragraph. The claims have been amended and are now believed to be allowable under 35 USC 112, second paragraph.

Claims 1-8 were rejected under 35 USC 102(b) as being anticipated by Hynecek. Claim 1 includes "... a bias tracking voltage reference generator coupled to the output diode diffusion for providing bias to the output diode diffusion ...". The references of record do not show, teach, or suggest the above limitations of claim 1. Hynecek (US 2002/0017646) does not disclose the bias tracking voltage reference generator of claim 1. Voltage V_{OG} in Hynecek is comparable to bias voltage V_{OG} in Figure 1 of the present invention. Claims 2-8 depend from claim 1. Therefore, claims 1-8 are believed to be allowable over the references of record.

It is believed that the above remarks and amendments are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,



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